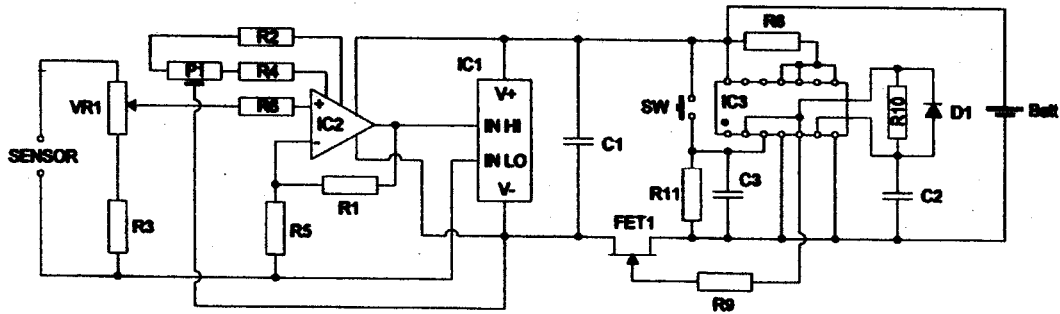
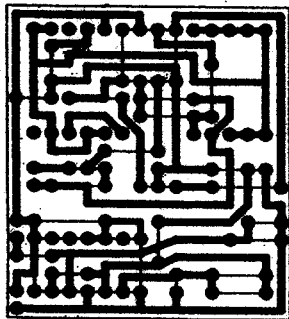


The schematic diagram illustrates a circuit board layout with the following components and connections:

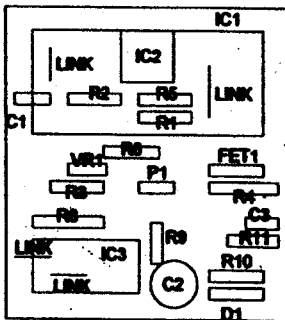
- IC1**: A large integrated circuit at the top right, containing **IC2** and two **LINK** sections.
- IC2**: An integrated circuit located inside **IC1**.
- LINK**: Two connection points, one on the left side of **IC1** and one on its right side.
- C1**: A capacitor connected to the left **LINK**.
- R2**, **R5**, and **R1**: Resistors connected to the bottom of **IC1**.
- R6**: A resistor located below **IC1**.
- FET1**: A field-effect transistor located to the right of **R6**.
- VR1**: A variable resistor located below **R6**.
- P1**: A component (possibly a potentiometer or pin header) located below **VR1**.
- R3**: A resistor located below **VR1**.
- R4**: A resistor located below **FET1**.
- R8**: A resistor located to the left of **IC3**.
- LINK**: A connection point on the left side of **IC3**.
- IC3**: An integrated circuit at the bottom left, containing another **LINK** section.
- R9**: A resistor located to the right of **IC3**.
- C2**: A capacitor located below **R9**.
- C3**: A capacitor located to the right of **R9**.
- R10** and **D1**: A resistor and a diode located below **C2**.
- R1** and **D2**: A resistor and a diode located to the right of **C3**.



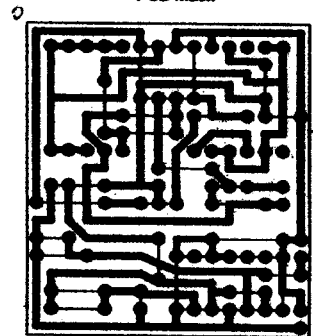
PCB layout



component sites



PCB mask



40 1/2 mm

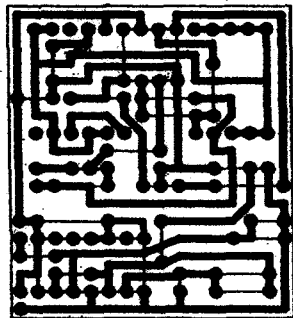
45 mm

2mm

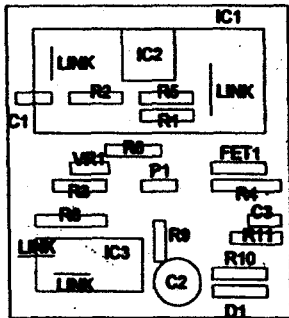
Scale
1:1.

New PCB Layout for Original VN202 circuit

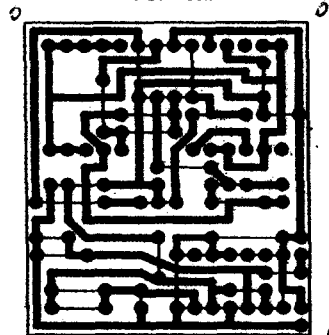
PCB layout



component sites



PCB mask



45mm
40 1/2mm
2mm