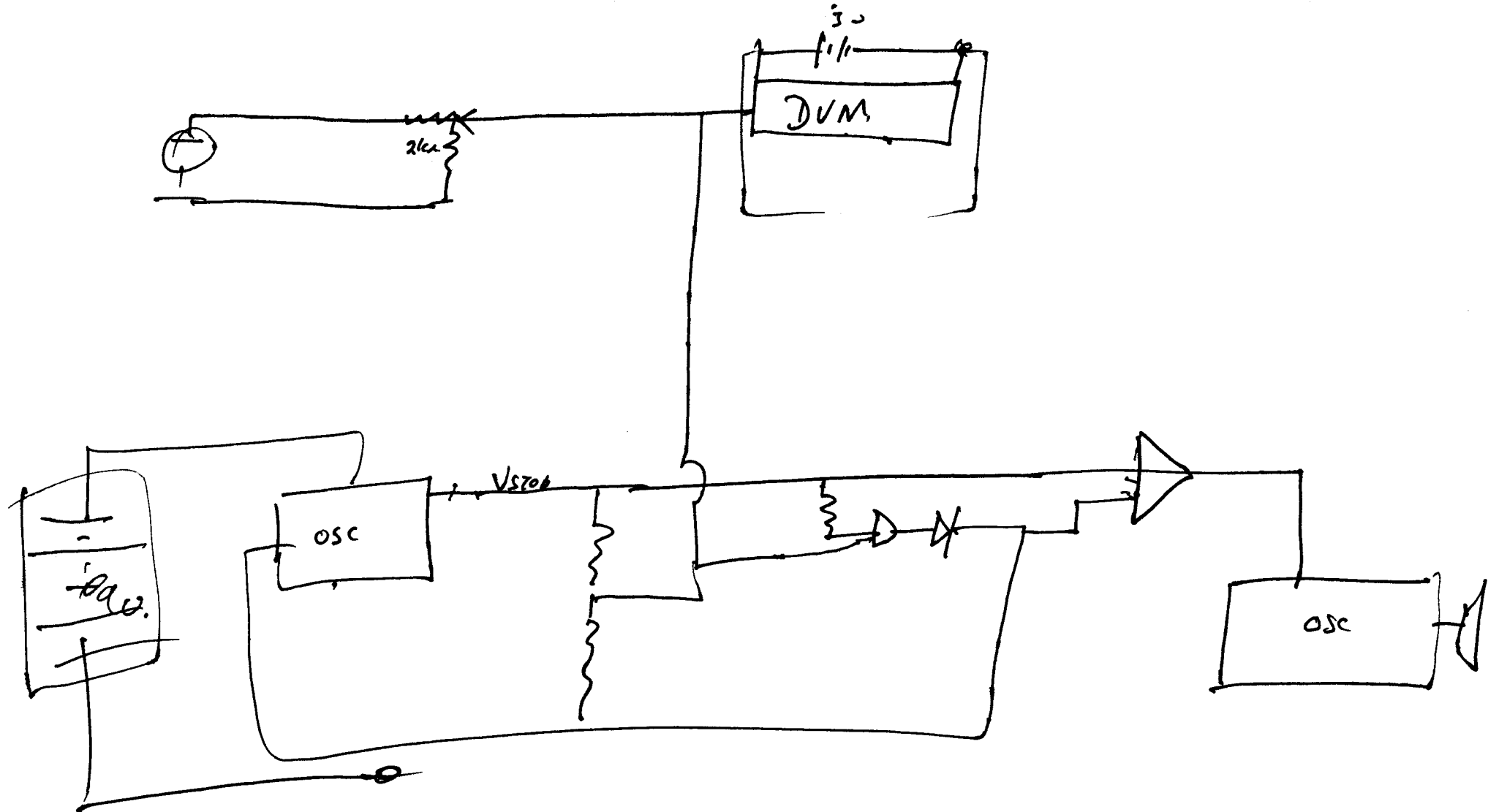
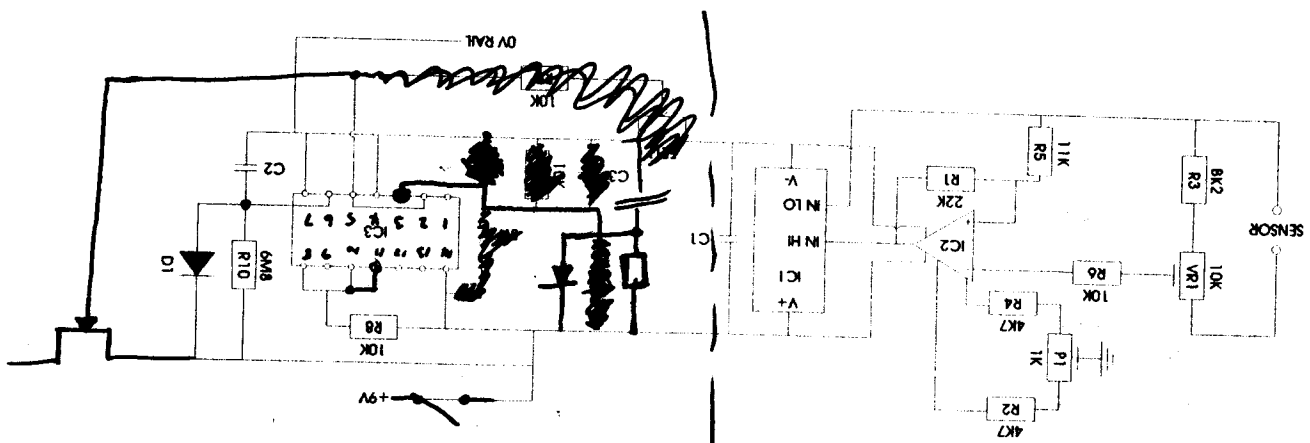




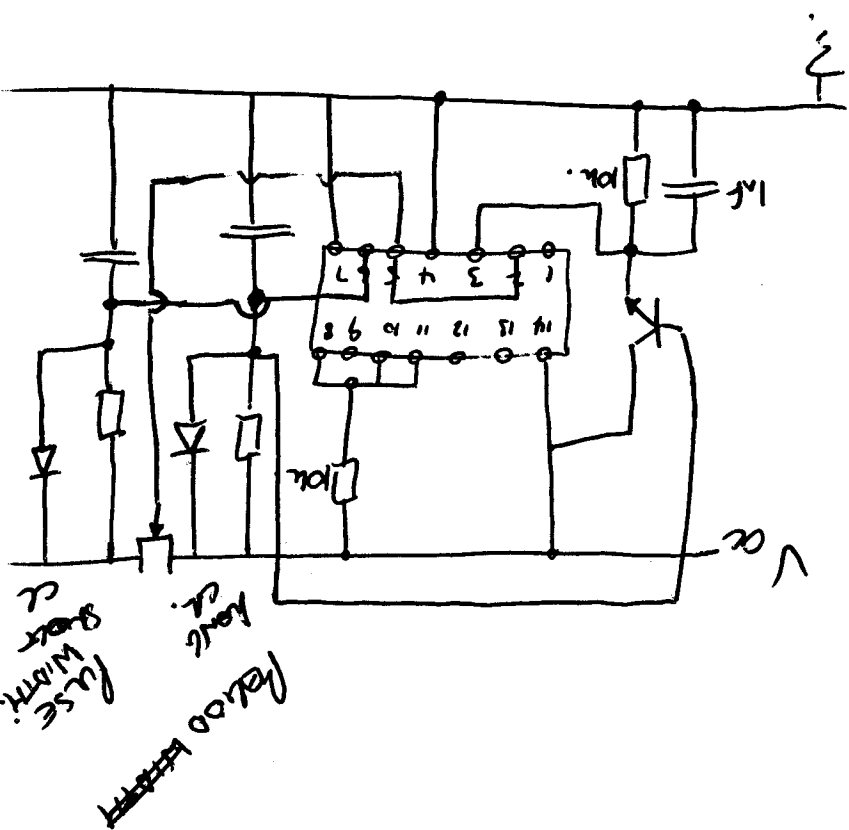
gll May 2000

Circuit Design Considerations

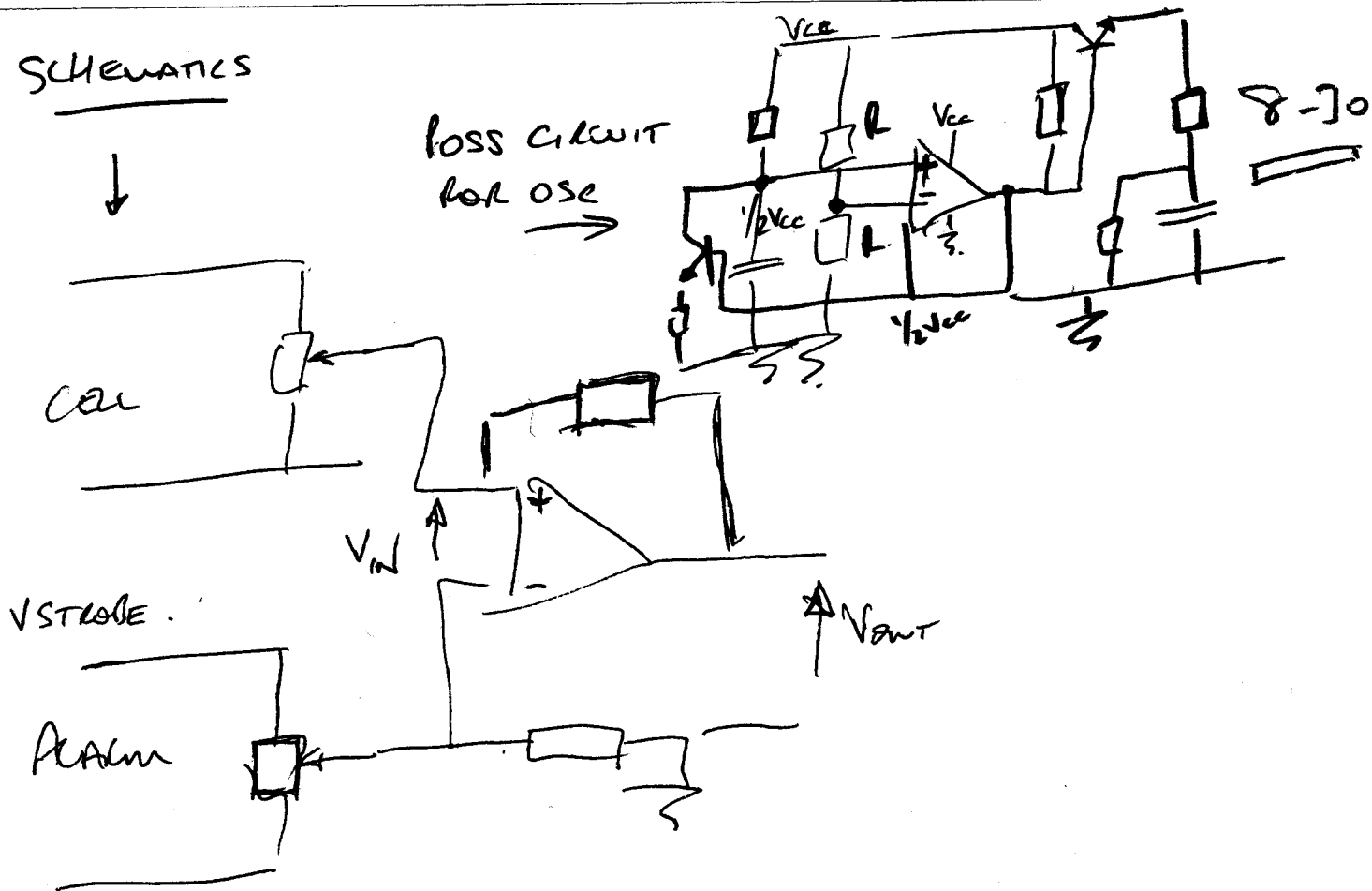
Circuit Schematic



R1	22K	C1	10nF	IC1	DPM400
R2	4K7	C2	33uF	IC2	TL071CP
R3	8K2	C3	1nF	IC3	MC14013BCN
R4	4K7	VR1	10K		
R5	11K	P1	1K Multi		
R6	10K	FET1	VN10KM		
R8	10K	D1	IN4148		
R9	10K				
R10	6M8				
R11	10K				



SCHEMATICS



~~1H~~. 57KS DURATION

Target : 43800 Ks. Cell cap 1AH.

Target average current draw = 22.83 μ A.

4.54 μ A @ 2.2s period / 50ms pulse + internal

3 1/3 μ A @ 3s period / 50ms pulse + internal

0.625 μ A @ 20s period / 50ms pulse + internal

238 Ω
344 Ω

0.473 702 155