

Subject : FW: preliminary BluxOxy Schematic
Date : Tue, 25 May 2004 16:57:00 +0100
Linked to: Kh Hui
From : khhui@automatic.com.hk (By way of export@viamed.co.uk)
To : JSLAMB (John Lamb) <GoldMine User>

Dear John,
Please find the schematic for ref and comment, Please let us know your comment if it is ok. We are working on the BOM and cost on the other hand.

We plan to visit Viamed for preliminary business discussion with you, Derek and Steve on earlier possible June 1 and 2. Please advise if you all will be available on the time.

Regards,
KH

-----Original Message-----

From: stephen [<mailto:stephenng@automatic.com.hk>]
Sent: Wednesday, May 19, 2004 7:02 PM
To: MDZ-MKT-SAMUELSHAM
Cc: MDZ-MGT-KHHUI; MDZ-MKT-KEVINTANG
Subject: preliminary BluxOxy Schematic

Dear Samuel,

Please find the newly designed schematic in attachment, The design implement a Low cost DMM IC,

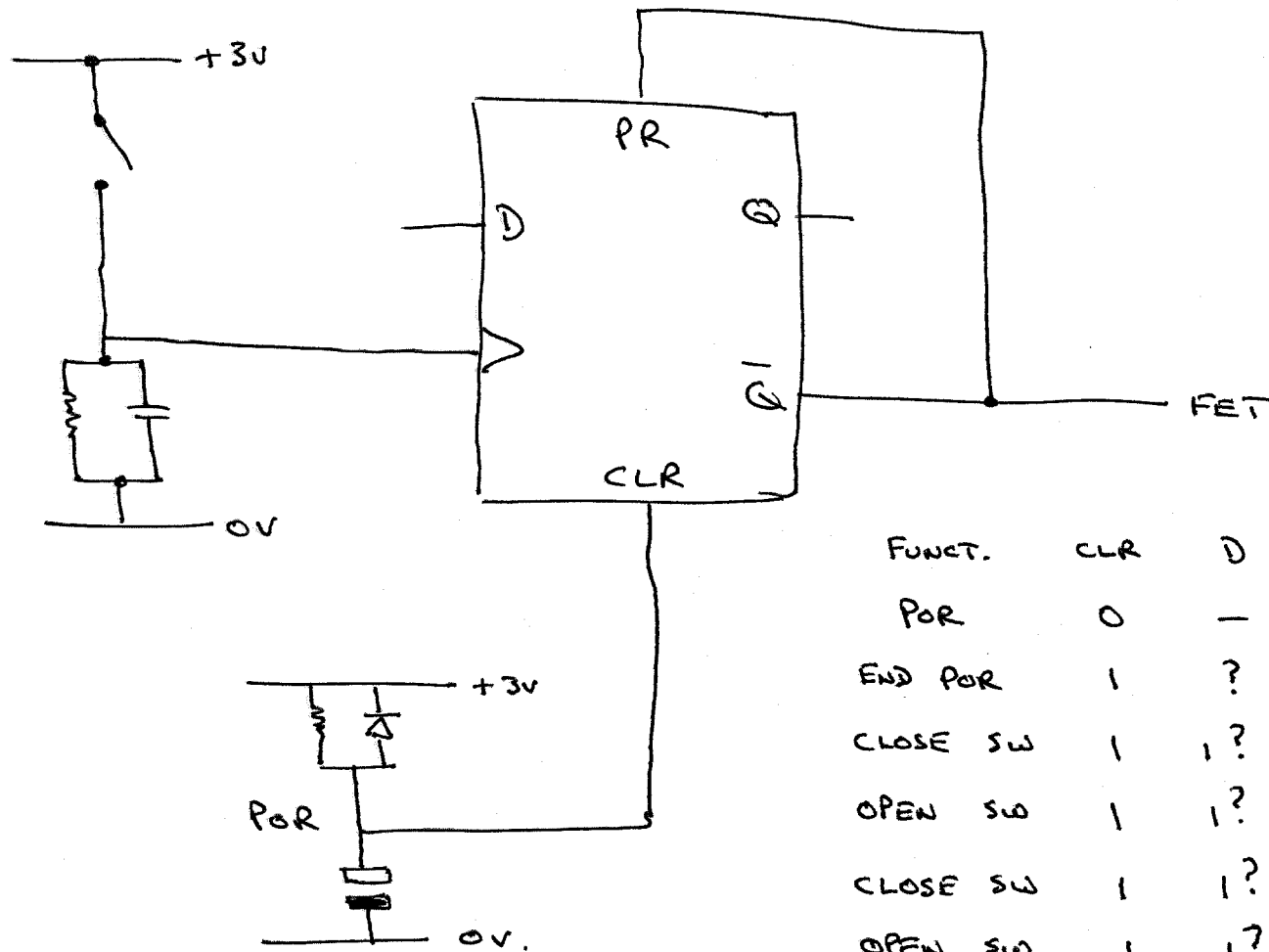
features like :

1. Low cost (single chip DMM IC)
2. 2000 Count, Internal reference 100ppm/C
3. Can use 3V battery
4. auto switch off
5. backlight (minutes)

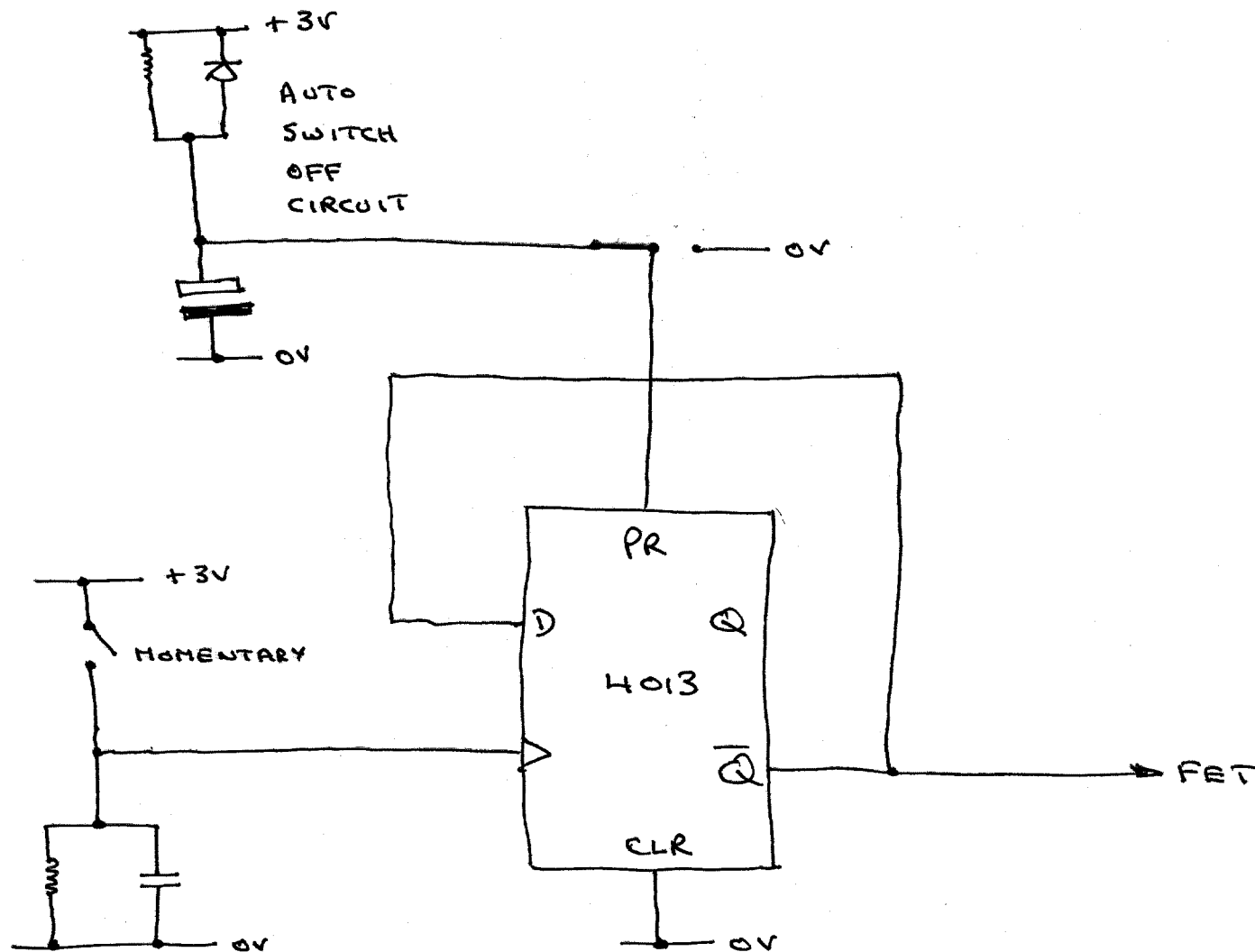
Br,

Stephen Ng

AML PROPOSED



FUNCT.	CLR	D	Q	$\overline{Q}/PR$	CLK.
POR	0	—	0	1	—
END POR	1	?	0	1	0
CLOSE SW	1	1?	1	0	↑
OPEN SW	1	1?	1	0	↓
CLOSE SW	1	1?	1	0	↑
OPEN SW	1	1?	1	0	↓



VN202 EXISTING

NOTE: NO CMOS INPUTS ARE TO BE LEFT FLOATING.

AML Padusac

